

# Austin TLynx<sup>™</sup> Series Evaluation Board Documentation

#### Introduction

There are three Austin TLynx<sup>TM</sup> series evaluation boards. One board has a dual layout of the Austin PicoTLynx<sup>TM</sup> and the Austin MicroTLynx<sup>TM</sup> / ProLynx<sup>TM</sup> modules, the second has a dual layout of the Austin MicroTLynx<sup>TM</sup> / ProLynx<sup>TM</sup> and the Austin TLynx<sup>TM</sup> / MegaTLynx<sup>TM</sup> modules and the third has a single layout of the GigaTLynx modules. Note that the boards come with a module already assembled on to the board. The specific combination of module and board desired can be ordered using the list available from your sales representative.

The dual layouts on each board also serve as example layouts for applications where dual layouts may be desirable, e.g. when the output current is uncertain and there is an advantage in either adding a higher power or lower power capability through a dual layout.

### **1. Schematics**

Figures 1 and 2 show schematics of the PicoTLynx/MicroTLynx/ProLynx and MicroTLynx/ProLynx/TLynx/MegaTLynx evaluation boards. Figure 3 shows the Giga TLynx board.



Figure 1. Schematic of the PicoTLynx/MicroTLynx/ProLynx Evaluation board







Figure 2. Schematic of the MicroTLynx/ProLynx/TLynx/MegaTLynx Evaluation board



Figure 3. Schematic of the GigaTLynx Evaluation board



### 2. Physical Descriptions

An annotated photograph of the PicoTLynx/MicroTLynx/ProLynx evaluation board is provided in the figure below. The notes indicate locations of various components. A minimum list of external components are the trim resistor R34/R35, input filtering a ( $1\mu$ F + 2 x 22 $\mu$ F,  $16V_{min}$ ) ceramic capacitors are recommended as a minimum and are already assembled on the board) and some modest amount of output filtering ( $1\mu$ F+ $10\mu$ F ceramic). Please refer to module datasheet for minimum specified capacitance.



Figure. 4. PicoTLynx/MicroTLynx/ProLynx evaluation board

**Caution!** Before applying power, make sure that the unit under test and the externally installed capacitors (input & output) have appropriate voltage ratings.

## Notes: Module can be trimmed either by soldering fixed resistor(s) @ R34/R35 or by attaching a trim potentiometer/resistor between test points E16 and E26.

\* 50V cap rating for ProLynx modules

\*\*For ProLynx module, On/Off should be shorted to ground for module to turn-on



An annotated photograph of the MicroTLynx/ProLynx/TLynx/MegaTLynx evaluation board is provided in the figure below. The notes indicate locations of various components. A minimum list of external components are the trim resistor R34/R35, input filtering a (1µF + 2 x 22µF, 16V/50V<sub>min</sub>) ceramic capacitors are recommended as a minimum and are already assembled on the board) and some modest amount of output filtering (1µF+10µF ceramic). Please refer to module datasheet for minimum specified capacitance. The ProLynx board needs at least 50V rated input caps.



Figure 5. MicroTLynx/ProLynx/TLynx/MegaTLynx evaluation board

**Caution!** Before applying power, make sure that the unit under test and the externally installed capacitors (input & output) have appropriate voltage ratings.

### Notes: Module can be trimmed either by soldering fixed resistor(s) @ R34/R35 or by attaching a trim potentiometer/resistor between test points E16 and E26.

\* 50V cap rating for ProLynx modules

\*\*For ProLynx module, On/Off should be shorted to ground for module to turn-on



An annotated photograph of the Giga TLynx evaluation board is provided in the figure below. The notes indicate locations of various components. A minimum list of external components are input filtering (4 x 22µF, 16V<sub>min</sub>) ceramic capacitors are recommended as a minimum and are already assembled on the board) and some modest amount of output filtering (1µF+2x10µF ceramic, 4V<sub>min</sub>). Please refer to module datasheet for minimum specified capacitance.



Figure 6. Giga TLynx evaluation board

**Caution!** Before applying power, make sure that the unit under test and the externally installed capacitors (input & output) have appropriate voltage ratings.



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